Cisco ASR 9900 Route Processor 2 Data Sheet

Contact Us
Phone: +852-51736677
Skype: wendycisco
WhatsAPP: +852-51736677
E-mail: wendy@donewin.com.hk

Contents

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The Cisco® ASR 9900 Route Processor 2 (ASR 9900 RP2) is the next-generation system processor for the Cisco ASR 9912 Router and ASR 9922 Router, supporting high-density 100 Gigabit Ethernet line cards (Figure 1). The Cisco ASR 9900 RP2 system architecture is designed to accommodate new programmable deployment models and convergence of Layer 2 and Layer 3 services, as required by today's wireline, Data-Center-Interconnect (DCI), and Radio Access Network (RAN) aggregation applications.

The ASR 9900 RP2 brings the time-tested and robust carrier-class capabilities of Cisco IOS® XR Software to the Carrier Ethernet edge. The operating system supports true software process modularity, and it allows each process to run in separate protected memory, including each routing protocol, along with multiple instances of control, data, and management planes supported. The software also supports distributed route processing.

Figure 1.
Cisco ASR 9900 Route Processor 2

The Cisco ASR 9900 RP2 is designed to deliver the high scalability, performance, and fast convergence required for today's and tomorrow's demanding video, cloud, and mobile services. These features provide exceptional scale, service flexibility, and high availability:

- Switch fabric architecture along with Cisco ASR 9900 Fabric Cards:

  ◦ Distributed switch fabric architecture

  ◦ Control of up to seven Cisco ASR 9900 Switch Fabric Cards to provide scalability and high availability on Cisco ASR 9922 Router and ASR 9912 Router chassis

  ◦ Multistage low-latency nonblocking architecture

  ◦ Service intelligence and traffic prioritization

- Superior network timing capabilities with support for:

  ◦ Centralized Building Integrated Timing Supply (BITS)

  ◦ Precision Time Protocol (PTP), or IEEE 1588-2008, through dedicated 10-Mbps and 100-Mbps Ethernet port

  ◦ Bidirectional Time of Day (ToD) with 10-MHz and 1-pps interface

Route-Processor types

The Cisco ASR 9900 Route Processor is available in service-edge-optimized and packet-transport-optimized models. The service-edge-optimized version offers the higher amount of memory that is essential for large-scale comprehensive service deployment. Both versions of the route processor support service-optimized, as well as transport-optimized, line cards. Different line cards can be mixed on the same chassis for greater flexibility.

Features and benefits of the ASR 9900 RP2 are listed in Table 1.

Table 1. Features and benefits of ASR 9900 RP2

Feature	Benefit
Highly scalable fabric	• Designed to support high 1-, 10-, and 100-Gbps port densities • Provides built-in scalability for investment protection
Control of up to seven switch fabric cards	Offers traffic load balancing simultaneously across up to seven fabrics
Distributed-forwarding-plane architecture	Allows line cards to support independent forwarding for enhanced performance and scale
Memoryless switch fabric	Provides transparent nonblocking, low-latency packet forwarding
Virtual output queuing and arbitration	• Offers service intelligence with prioritization of traffic (unicast and multicast) • Provides efficient congestion-management mechanism and avoids problems related to head-of-line blocking
Centralized arbiter	Uses an efficient credit mechanism to help ensure transparent switchover with zero packet loss
IEEE 1588 support	Delivers timing services over the packet network efficiently and reliably
Two independent clock source connections: BITS and Synchronization Supply Unit (SSU) DTI	Offers redundant, centralized network synchronization support
Two 32-GB Solid-State Drives (SSDs)	Allows storing of core dumps and helps reduce the system Mean Time To Repair (MTTR)
Embedded Universal Series Bus (eUSB) memory port	Provides access to onboard Universal Serial Bus (USB) flash-memory devices for software image storing and upgrades
Front-panel external USB 2.0 port	Provides access to USB flash-memory devices for quick software image loading and recovery
Front-panel LEDs	Provides visual indication of route-processor status (active or standby), power management, and activity on SSD
Management ports	Provides easy access to system console
Processor	Has 8 cores, 1.9 GHz

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Table 2 lists all the hardware available for the ASR 9900 RP2.

Table 2. ASR 9900 RP2 Hardware

Product Number	Product Description
A99-RP2-TR	ASR 9900 Route Processor 2 for Packet Transport 16G
A99-RP2-SE	ASR 9900 Route Processor 2 for Service Edge 32G

Table 3 lists the technical specifications for the ASR 9900 RP2.

Table 3. Technical Specifications for ASR 9900 RP2 Hardware

Technical Specifications
Internal Memory
• Control of up to seven Cisco ASR 9900 switch fabric cards • ASR 9900 Route Processor 2 for Packet Transport 16G (product number: A99-RP2-TR): 16-GB Error-Correcting Code (ECC)-protected DRAM • ASR 9900 Route Processor 2 for Service Edge 32G (A99-RP2-SE): 32-GB ECC-protected DRAM • Solid-state disk: Two 32-GB SSDs • 8-GB embedded USB • USB 2.0 Type A receptacle
Timing System
• Timing: Two independent clock-source connections • IEEE 1588 support: Copper 10-Mbps and 100-Mbps RJ-45 Ethernet port
GPS
• ToD (RS-422 and RS-232) • 1-pps RS-422 or 1.0/2.3 50-ohm RF connector • 10-MHz in/out 1.0/2.3 50-ohm RF connector
Management
• Two 100/1000BASE-T (RJ-45) LAN management ports • One console port • One auxiliary port
Alarms
• Alarm outputs: Critical alarm (CR), major alarm (MJ), and minor alarm (MN)
LEDs
• Amber Alarm Cutoff (ACO) and lamp test • System Synchronization alarm (SYNC) • Global Positioning System (GPS) • Fabric-card fault indicator • SSD

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Software

The Cisco ASR 9000 Series Aggregation Services Router delivers superior scale, service flexibility, and high availability into access and aggregation networks. It is powered by Cisco IOS XR Software, an innovative self-healing, distributed operating system designed for always-on operation. Cisco IOS XR Software supports a Software-Maintenance-Update (SMU) capability, which allows bug fixing or even small feature releasing without interrupting existing services. It also supports Field-Programmable Device (FPD) upgrades, which can be used to update Field-Programmable Gate Arrays (FPGAs), ROM monitor (ROMmon), and more, while systems are running.

Cisco ASR 9000 Series Carrier Ethernet applications include business services such as Layer 2 VPN (L2VPN) and Layer 3 VPN (L3VPN), Internet Protocol Television (IPTV), Content-Delivery Networks (CDNs), and mobile backhaul transport networks. Features supported include Ethernet Services; L2VPN; IPv4, IPv6, and L3VPN; Layer 2 and Layer 3 Multicast; IP over Dense Wavelength-Division Multiplexing (IPoDWDM); SyncE; Ethernet Operations, Administration, and Management (EOAM) and Multiprotocol Label Switching (MPLS) Operations, Administration, and Management (OAM); Layer 2 and Layer 3 Access Control Lists (ACLs); Hierarchical Quality of Service (H-QoS); MPLS Traffic Engineering Fast Reroute (MPLS TE-FRR); Multichassis Link Aggregation (MC-LAG); Integrated Routing and Bridging (IRB); Cisco Nonstop Forwarding (NSF) and Nonstop Routing (NSR); Point-to-Multipoint Traffic Engineering (P2MP-TE); Lawful Intercept; Smart Call Home (SCH); and Multigigabit Service Control (MGSCP).

The Cisco ASR 9000 Series Multiservice Edge (MSE) and Ethernet MSE (E-MSE) capabilities allow enterprises to offer powerful business VPN services with strong Service-Level Agreement (SLA) enforcement. Such services typically require simultaneous scale increases across multiple dimensions, for example, the number of Virtual Route Forwarding (VRF) interfaces, IPv4 and IPv6 route scaling, Bidirectional Forwarding Detection (BFD) sessions, and instances of Border Gateway Protocol (BGP) Cisco NSR interfaces. A Cisco ASR 9900 system configuration requiring high multiple-dimensional scale requires the Service-Edge (SE) optimized route processor model to support the increased system scale.

Timing synchronization is an integral part of traditional circuit-based networks, so the availability of equivalent functions in next-generation Ethernet-based architectures has quickly become a critical requirement. The Cisco ASR 9900 chassis have standards-compliant Precision Time Protocol Version 2 (PTPv2), GPS, DTI, and BITS connections on the route processor, and SyncE support natively on the line cards, giving mobile providers ample options for time and frequency synchronization. Additionally, the Cisco ASR 9900 Route Processor supplies centralized clocking functions throughout the system, providing consolidated timing distribution and recovery to and from the line cards.

Product specifications

Table 4 provides details about the ASR 9900 RP2, which supports the Cisco ASR 9912 and Cisco ASR 9922 chassis, therefore providing common sparing. Both Cisco ASR 9922 and ASR 9912 systems are designed to the same high standards of performance and reliability. They feature the same power and thermal innovations, and they can share route processors, line cards, Power Entry Modules (PEMs), and power supplies, for greater flexibility in your network planning.

Table 4.   Product specifications

Category	Part Number or Specification	
Route processor	A99-RP2-TR A99-RP2-SE For simplicity and common sparing, this route processor is introduced on Cisco IOS XR Release 5.3.0, and it supports both Cisco ASR 9912 and ASR 9922 chassis.	
Part numbers of supported line cards	**5th Generation** • **A9K-20HG-FLEX-FC** • A9K-20HG-FLEX-SE • A9K-20HG-FLEX-TR • A9K-8HG-FLEX-FC • A9K-8HG-FLEX-SE • A9K-8HG-FLEX-TR • **A99-4HG-FLEX-FC** • A99-4HG-FLEX-SE • A99-4HG-FLEX-TR • **A9K-4HG-FLEX-FC** • A9K-4HG-FLEX-SE • A9K-4HG-FLEX-TR **4th Generation** • **A99-32X100GE-FC** • A99-32X100GE-TR • A99-32X100GE-CM • **A9K-16X100GE-FC** • A9K-16X100GE-TR • A9K-16X100GE-CM • **A99-16X100GE-X-FC** • A99-16X100GE-X-SE	**3rd Generation** • **A99-12X100GE-FC** • A99-12X100GE • A99-12X100GE-CM • **A99-8X100GE-FC** • A99-8X100GE-SE • A99-8X100GE-TR • A99-8X100GE-CM • **A9K-8X100GE-FC** • A9K-8X100GE-SE • A9K-8X100GE-TR • A9K-8X100GE-CM • A9K-8X100G-LB-SE • A9K-8X100G-LB-TR • **A9K-4X100GE-FC** • A9K-4X100GE-SE • A9K-4X100GE-TR • **A9K-400GE-LAN-FC** • A9K-4X100GE • **A99-48X10GE-1G-FC** • A99-48X10GE-1G-TR • A99-48X10GE-1G-SE • **A9K-48X10GE-1G-FC** • A9K-48X10GE-1G-TR • A9K-48X10GE-1G-SE • **A9K-48X10GE-1G-CM** • **A9K-24X10GE-1G-FC** • A9K-24X10GE-1G-TR • A9K-24X10GE-1G-SE • **A9K-24X10GE-1G-CM** • **A9K-MOD400-FC**

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Category	Part Number or Specification
	• A9K-MOD400-SE
	• A9K-MOD400-TR
	• A9K-MOD400-CM
	• **A9K-MOD200-FC**
	• A9K-MOD200-SE
	• A9K-MOD200-TR
	• A9K-400G-DWDM-TR
	2nd Generation
	• A9K-2X100GE-TR
	• A9K-2X100GE-SE
	• A9K-1X100GE-TR
	• A9K-1X100GE-SE
	• A9K-36X10GE-TR
	• A9K-36X10GE-SE
	• A9K-24X10GE-TR
	• A9K-24X10GE-SE
	• A9K-4T16GE-SE
	• A9K-4T16GE-TR
	• A9K-40GE-SE
	• A9K-40GE-TR
	• A9K-MOD160-SE
	• A9K-MOD160-TR
	• A9K-MOD80-SE
	• A9K-MOD80-TR
	• A9K-VSM-500
Redundancy	• No single point of failure • Route-processor redundancy (both route processors must be of the same kind) • Software redundancy
Physical specifications	Each route processor occupies 1 slot; a redundant-route-processor configuration occupies 2 slots in Cisco ASR 9922 and ASR 9912 chassis. • Height: 1.81 in. (4.60 cm) • Width: 16.87 in. (42.85 cm) • Depth: 24.74 in. (62.89 cm) • Weight: 17.8 lb (8.07 kg)
Environmental conditions	• Operating temperature: 32 to 104°F (0 to 40°C) • Storage temperature: -40 to 167°F (-40 to 75°C) • Relative humidity: 10 to 90%, noncondensing

Category	Part Number or Specification
Environmental Specifications	
Operating temperature (short-term)	23 to 131°F (–5 to 55°C) **Note:** Short-term refers to a period of not more than 96 consecutive hours and a total of not more than 15 days in 1 year (a total of 360 hours in any given year, but no more than 15 occurrences during that 1-year period).
Operating humidity (nominal) (relative humidity)	10 to 85%
Operating humidity (short-term)	5 to 90% **Note:** Not to exceed 0.024 kg water or dry air
Storage temperature	–40 to 158°F (–40 to 70°C)
Storage (relative humidity)	5 to 95% **Note:** Not to exceed 0.024 kg water or dry air
Operating altitude	–60 to 4000m (up to 2000m conforms to IEC/EN/UL/CSA 60950 requirements)
Compliance	
Network Equipment Building Standards (NEBS)	Cisco ASR 9900 platforms are designed to meet these standards: • SR-3580: NEBS Criteria Levels (Level 3) • GR-1089-CORE: NEBS Electromagnetic Compatibility (EMC) and Safety • GR-63-CORE: NEBS Physical Protection • VZ.TPR.9205: Verizon TEEER
ETSI standards	Cisco ASR 9900 platforms are designed to meet these standards (qualification in progress): • EN300 386: Telecommunications Network Equipment (EMC) • ETSI 300 019 Storage Class 1.1 • ETSI 300 019 Transportation Class 2.3 • ETSI 300 019 Stationary Use Class 3.1 • EN55022: Information Technology Equipment (Emissions) • EN55024: Information Technology Equipment (Immunity) • EN50082-1/EN-61000-6-1: Generic Immunity Standard
EMC standards	Cisco ASR 9900 platforms are designed to meet these standards: • FCC Class A • ICES 003 Class A • AS/NZS 3548 Class A • CISPR 22 (EN55022) Class A • VCCI Class A • BSMI Class A • IEC/EN 61000-3-2: Power Line Harmonics • IEC/EN 61000-3-3: Voltage Fluctuations and Flicker • EN 50121-4: Railway EMC

Category	Part Number or Specification
Immunity	Cisco ASR 9900 platforms are designed to meet these standards: • IEC/EN-61000-4-2: Electrostatic Discharge Immunity (8kV Contact, 15kV Air) • IEC/EN-61000-4-3: Radiated Immunity (10V/m) • IEC/EN-61000-4-4: Electrical Fast Transient Immunity (2kV Power, 1kV Signal) • IEC/EN-61000-4-5: Surge AC Port (4kV CM, 2kV DM) • IEC/EN-61000-4-5: Signal Ports (1kV) • IEC/EN-61000-4-5: Surge DC Port (1kV) • IEC/EN-61000-4-6: Immunity to Conducted Disturbances (10Vrms) • IEC/EN-61000-4-8: Power Frequency Magnetic Field Immunity (30A/m) • IEC/EN-61000-4-11: Voltage DIPS, Short Interruptions, and Voltage Variations • EN 50121-4: Railway EMC
Safety	Cisco ASR 9900 platforms are designed to meet these standards: • UL/CSA/IEC/EN 60950-1 • IEC/EN 60825 Laser Safety • ACA TS001 • AS/NZS 60950 • FDA: Code of Federal Regulations Laser Safety

Cisco Services for Cisco ASR 9000 Series Route Processors

Through a lifecycle services approach, Cisco delivers comprehensive support to service providers to help them successfully deploy, operate, and optimize their Cisco Prime™ Evolved Programmable Networks. Cisco Services for Cisco ASR 9000 Series Aggregation Services Routers provide the services and proven methodologies that help assure service deployment with substantial return on investment, operational excellence, optimal performance, and high availability. These services are delivered using leading practices, tools, processes, and lab environments developed specifically for Cisco ASR 9000 Series deployments and post-implementation support. The Cisco Services team addresses your specific requirements, mitigates risk to existing revenue-generating services, and helps accelerate time to market for new network services.

Ordering information

Table 5 provides ordering information for the Cisco ASR 9900 Route Processor.

Table 5.   Ordering information

Product Description	Supported Software Release	Part Number
Route processor optimized for packet transport	Cisco IOS XR Software Release 5.3.0 and later	A99-RP2-TR
Route processor optimized for packet transport, spare	Cisco IOS XR Software Release 5.3.0 and later	A99-RP2-TR =
Route processor optimized for service edge	Cisco IOS XR Software Release 5.3.0 and later	A99-RP2-SE
Route processor optimized for service edge, spare	Cisco IOS-XR Software Release 5.3.0 and later	A99-RP2-SE =

To place an order, visit Cisco Ordering Homepage and refer to Table 5.

Cisco environmental sustainability

Information about Cisco's environmental sustainability policies and initiatives for our products, solutions, operations, and extended operations or supply chain is provided in the "Environment Sustainability" section of Cisco's Corporate Social Responsibility (CSR) Report.

Reference links to **information about key environmental sustainability topics** (mentioned in the "Environment Sustainability" section of the CSR Report) are provided in the following table:

Sustainability topic	Reference
Information on product material content laws and regulations	Materials
Information on electronic waste laws and regulations, including products, batteries, and packaging	WEEE compliance

Cisco makes the packaging data available for informational purposes only. It may not reflect the most current legal developments, and Cisco does not represent, warrant, or guarantee that it is complete, accurate, or up to date. This information is subject to change without notice.

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Document history

New or Revised Topic	Described In	Date
Updated part numbers of supported line cards	Product Specifications	March 25, 2022

Contact Us
Phone: +852-51736677
Skype: wendycisco
WhatsAPP: +852-51736677
E-mail: wendy@donewin.com.hk