



H3C S12500X-AF Data Center Cloud Core Series Switches

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New H3C Technologies Co., Limited

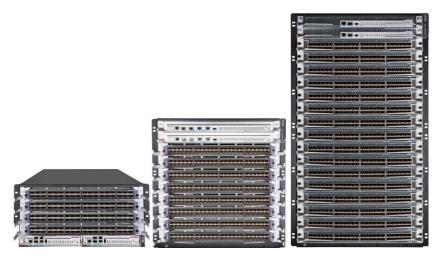


Overview

The H3C S12500X-AF DataCenter Cloud Core Switch Series is designed for cloud services data centers. It provides the following features:

- CLOS+ multi-grade multi-plane architecture
- With 768 line-speed 40G/100G interface per chassis and up to 9.6Tbps switch capacity per slot
- Integration of IRF2 (Intelligent Resilient Framework version 2), and MDC (Multi-tenant Device Context) to implement virtual resource pools
- Distributed ingress buffers (200 ms) to accommodate burst traffic in data centers
- Independent control, detection, and maintenance engines to implement 50ms failover and powerful control capabilities

The S12500X-AF switch series includes S12504X-AF, 12508X-AF and S12516X-AF, which meet various port density and performance requirements. The S12500X-AF switch series can work with H3C routers, switches, security devices, IMC, and H3Cloud to provide a wide variety solutions



H3C S12500X-AF Switch Series

Advanced CLOS+ multi-grade multi-plane switching architecture

 CLOS+ multi-grade multi-plane architecture, midplane free design, providing continuous bandwidth upgrade capability



- Supports industry first 48-port 40GE/ 100GE interfaces and can meet the existing and future application requirements of data centers
- Adopts independent switching fabric modules and MPU engines to improve device availability and ensure bandwidth expansion

Virtualization technologies – IRF2

IRF2 can virtualize up to two S12500X-AF switches into one logical IRF fabric. IRF2 delivers the following benefits:

- High Availability (HA) Patented hot standby technology provides data backup and non-stop forwarding on the control plane and data plane. It improves availability and performance, eliminates single-point of failures, and ensures service continuity
- Distribution Multi-chassis link aggregation to enable load sharing and backup over multiple uplinks, improving redundancy and link utilization
- Easy Management A single IP address to manage the whole IRF fabric, which simplifies device and topology management, improving operating efficiency, and lowering network maintenance cost

Virtualization technologies - MDC

 MDC virtualizes one S12500X-AF switch into multiple logical switches, enabling multiple services to share one core switch. The 1:N virtualization maximizes switch utilization, reduces network TCO, and ensures secure isolation of services

DC-oriented features

- EVI—EVI is a MAC-in-IP technology that provides Layer 2 connectivity between distant Layer 2
 network sites across an IP routed network. It is used for connecting geographically dispersed sites
 of a virtualized large-scale data center that requires Layer 2 adjacency
- FCOE—Integrates heterogeneous LANs and storage networks in data centers. FCOE and CEE
 integrate data, computing, and storage networks in data centers, reducing the costs for building
 and expanding data centers
- VXLAN (Virtual Extensible LAN) —VXLAN uses a MAC-in-UDP encapsulation method where the
 original Layer 2 package is added with a VXLAN header, and is then placed in a UDP-IP packet.
 With the help of MAC-in-UDP encapsulation, VXLAN tunnels Layer 2 network over Layer 3 network
 which provides two major benefits: higher scalability of Layer 2 segmentation and better utilization
 of available network paths



- MP-BGP EVPN (Mutliprotocol Border Gateway Protocol Ethernet Virtual Private Network) MP-BGP EVPN uses standard-based BGP protocol as the control plane for VXLAN overlay networks, providing BGP based VTEP auto peer discovery and end-host reachability information distribution.
 MP-BGP EVPN delivers many benefits, such as eliminating traffic flooding, reducing full mesh requirements between VTEPs via the introduction of BGP RR, achieving optimal flow based end to end load sharing and more
- Large capacities for storing ARP/ND, MAC, and ACL entries

Innovative multi-engine design

- Independent control, detection, and maintenance engines provide powerful control capability and millisecond-level HA:
- Independent control engine— Uses a powerful CPU system that can efficiently process protocol
 and control packets, providing refined control for protocol packets and comprehensive protection
 against protocol packet attacks
- Independent detection engine— Provides highly reliable Fast Fault Detection and Restoration (FFDR) such as BFD and OAM, which can interact with protocols on the control plane to implement millisecond-level failover and convergence, ensuring service continuity
- Independent maintenance engine—Uses an intelligent Embedded Maintenance Subsystem (EMS), a CPU system that provides smart power management, including sequential power-on and power-off and device status check. Sequential power-on and power-off reduces power impulse, electromagnetic radiation, power consumption, and extends the device lifespan

DC-class HA

FFDR provides BFD and OAM functions to implement fast failover and convergence. The following lists the DC-class HA features:

- BFD for VRRP/BGP/IS-IS/RIP/OSPF/RSVP/static routing
- NSR/GR for OSFP/BGP/IS-IS/RSVP
- Separation of control and data planes through independent control engine and switching fabric module.
- 1+1 redundancy for control engines
- N+1 redundancy for switch fabric modules
- 1+1 redundancy for fan trays
- N+M redundancy for power modules



Multi-level security protection

- The S12500X-AF switch series use QoS policies to filter and limit traffic from data plane to control
 plane. During a DoS attack, the switch can identify and protect important packets and discard
 attack packets, ensuring normal operation
- Supports a large numbers of ACLs while ensuring line-speed forwarding. ACLs can identify and control L2/IPv4/IPv6/MPLS traffic by using combinations of packet fields
- The S12500X-AF switch series supports hardware level encryption technology MACsec (802.1ae), which is an industry-standard security technology that provides secure communication for all traffic on Ethernet links.

Distributed buffering and precise QoS

- Distributed ingress buffers accommodate burst traffic. Each port performs a precise bandwidth
 assignment and traffic shaping for incoming traffic, and distributes the traffic to ingress buffers.
 Distributed buffering can fully utilize the buffers of line cards to ensure best buffering performance
- A network model change from C/S to B/S leads to increased volumes of burst traffic. Network
 devices must have larger buffering capabilities to support this. The S12500X-AF series supports 200
 ms buffering of burst traffic per 10G interface, which can meet the burst traffic requirements of
 large data centers
- Each chip can support 4GB buffer, maximum of 24GB buffer per line card
- Each line card supports a maximum of 96K hardware queues, refined QoS, and traffic management.

 QoS can assign different priorities and queues to different users to provide differentiated services

Comprehensive maintenance and monitoring

- Online state monitoring Uses a dedicated engine to monitor the state of switch fabric modules, backplane channels, service communication channels, key chips, and storage. Once a failure occurs, it reports the failure to the system through EMS
- Card isolation- Isolates specified cards from the forwarding plane. The isolated cards still work on the control plane, allowing the user to perform management operations such as real-time diagnosis and CPLD upgrade on the isolated cards without affecting system operation
- Ethernet OAM- Provides multiple device-level and network-level fault detection methods



Green

- Intelligent EMS engine system Provides smart power management that supports sequential
 power-on and power-off and device status check. Sequential power-on and power-off reduces
 power impulse and electromagnetic radiation, and increases the lifetime of the device. Additionally,
 device status checks can isolate faulty and idle cards to reduce power consumption
- Smart fan management- Collects fan temperature, calculates fan speed, and assigns the calculated speed to the fan tray. In addition, it detects fan speeds, fault alarms, and performs speed adjustment based on configuration sand area, reducing power consumption and noise, increasing the fan's lifetime
- Internal interface monitoring-Automatically shuts down unused internal interfaces to reduce power consumption
- RoHS compliance The S12500X-AF switch series meets the EU RoHS safety standards.
- The S12500X-AF switch series is designed with front to back air flow, satisfying highly efficient heat dissipation requirements in data center.

Hardware Specifications

Item	S12504X-AF	S12508X-AF	S12516X-AF
Switching capacity	57.6T/387Tbps	115.2T/516Tbps	230.4T/1032Tbps
Throughput	28800Mpps	57600Mpps	115200Mpps
MPU slots	2	2	2
LPU slots	4	8	16
Maximum power consumption	4800 W	9600 W	19200W
Weight (full configuration)	≤ 100 kg ≤ 220.5 lb	≤ 190 kg ≤ 418.9 lb	≤ 350 kg ≤ 771.6 lb
Dimensions (H x W x D)	264 x 440 x 857 mm (6U) 10.4 x 17.3 x 33.7 in	531 x 440 x 857 mm (12U) 20.9 x 17.3 x 33.7 in	931 x 440 x 857 mm (21U) 36.7 x 17.3 x 33.7 in
Switching fabric module slots	6	6	6



MPU Name	LSXM1SUP04B1	LSXM1SUP04H1	LSXM1SUPB1	LSXM1SUPH1	LSXM1SUPB1	LSXM1SUPH1
MPU processor	Quad Core 1.2 GHz		Quad Core 1.2 GHz		Quad Core 1.2 GHz	
MPU SDRAM	8 GB	16 GB	8 GB	16 GB	8 GB	16 GB
MPU Flash	1 GB		1 GB		1 GB	
MPU Console Port	1		1		1	
MPU MGMT Ports	2x 10/100/1000M Base-T 2x 1000M SFP		M Base-T	10/100/1000 M Base-T	2x 10/100/1000 M Base-T 2x 1000M SFP	1x 10/100/1000 M Base-T 1x 1000M SFP
MPU USB Port	1		1		1	
Redundancy	Redundant MPUs, switching fabric modules, power modules, and fan trays					

Software Specification

Item	Feature description
Device Virtualization	IRF
Natural Water limiting	BGP-EVPN
Network Virtualization	VxLAN
	L2 VxLAN gateway
	L3 VxLAN gateway
	Distributed VxLAN gateway
VxLAN	Centralized VxLAN gateway
	EVPN VxLAN
	manual configured VxLAN
	IPv4 VxLAN tunnel
Dragrammahilitu	Openflow1.3
Programmability	Netconf



Item	Feature description
	Ansible
	Python//TCL/Restful API to realize DevOps automated operation and maintenance
Traffic analysis	Sflow
	Port-based VLANs
\/ A N	VLAN mapping
VLAN	L2PT
	MVRP(Multiple VLAN Registration Protocol)
NAAC addasaa	Dynamic learning and aging of mac address entries
MAC address	Dynamic, static and blackhole entries
	RIP(Routing Information Protocol) v1/2
	OSPF (Open Shortest Path First) v1/v2
	ISIS(Intermediate System to Intermediate system)
IPv4 routing	BGP (Border Gateway Protocol)
(Hardware-based unicast routing)	Routing policy
unicust routing)	VRRP
	PBR
	ICMP
	RIPng
	OSPFv3
IPv6 routing	IPv6 ISIS
(Hardware-based	BGP4+
unicast routing)	Routing policy
	VRRP
	PBR
	IPv6 ICMP
IP Service	DHCP Server, DHCP Relay, DHCP Snooping
II SELVICE	DHCP server of 3K operations/second
	Support L3 MPLS VPN
MPLS/VPLS	Support MCE
	VPLS



Item	Feature description
	Support MPLS OAM
	Support P/PE function
	Support LDP protocol
	IGMP snooping
	MLD snooping
Multicast	IPv4 and IPv6 multicast VLAN
(Hardware-based	IPv4 and IPv6 PIM snooping
multicast routing)	IGMP and MLD
	PIM and IPv6 PIM, Any-RP
	MSDP
	LACP
	LACP local forwarding first
	LACP short-time
	LACP Stack split detection
	STP/RSTP/MSTP protocol, PVST compatible
	STP Root Guard and BPDU Guard
Daliah III.	RRPP and ERPS(ITU-T G.8032)
Reliability	Loopback detection
	Ethernet OAM
	Smartlink
	DLDP
	BFD for OSPF/OSPFv3, BGP/BGP4, IS-IS/IS-ISv6, PIM/IPM for IPv6 and Static route
	VRRP and VRRPE
	ERSPAN
Telemetry	Packet capture
	Weighted Random Early Detection (WRED) and tail drop
QOS	Flexible queue scheduling algorithms based on port and queue, including strict priority (SP), Weighted Deficit Round Robin (WDRR), Weighted Fair Queuing (WFQ), SP + WDRR, and SP + WFQ.
	Traffic shaping
	COPP



Item	Feature description
	Committed access rate (CAR)
	Account by packet and byte
QOS	Packet filtering at L2 (Layer 2) through L4 (Layer 4); flow classification based on source MAC address, destination MAC address, source IP (IPv4/IPv6) address, destination IP (IPv4/IPv6) address, port, protocol, and VLAN to apply qos policy,including mirroring,redirection,priority remark etc.
	Console telnet and SSH terminals
Configuration and maintenance	SNMPv1/v2/v3
maintenance	ZTP
	System log
	File upload and download via FTP/TFTP
Configuration and	BootRom update and remote update
maintenance	NQA
	ping,tracert
	NTP
	Hierarchical management and password protection of users
	Authentication methods, including AAA, RADIUS and HWTACACS
	Support DDos, ARP attack and ICMP attack function
	SSH 2.0
Security and	HTTPS
management	SSL
	PKI
	Boot ROM access control (password recovery)
	RMON
	permit third party transceivers (license)
	Independent switching fabric modules
	1+1 redundancy or key components such as MPUs and power modules
	N+1 redundancy for switching fabric modules
НА	Passive backplane
	CLOS+ midplane free design (12500X-AF)
	Hot swapping for all components
	Real-time data backup on active/standby MPUs



Item	Feature description
	Hot patching
	NSR/GR for OSFP/BGP/IS-IS/RSVP
НА	Port aggregation and multi-card link aggregation
.,,	BFD for VRRP/BGP/IS-IS/OSPF/RSVP/static routing, with a failover time less
	than 50 milliseconds
	IP FRR and TE FRR with a switchover time less than 50 millisecond
	802.3ab/802.3ae/802.3z/802.3x/802.3ad
IEEE Standard	802.3AH/802.1P/802.1Q/802.1X/802.1D/802.1w/802.1s/802.1AG
	802.1x/802.1Qbb/802.1az/802.1Qaz
RFC	RFC793/RFC2328/RFC1256/RFC1771/RFC1185/RFC1191/RFC1195/RFC1195/ RFC1212/RFC1213/RFC1213/RFC1213/RFC1215/RFC1245/RFC1246/RFC125 6/RFC1256/RFC1265/RFC1266/RFC1268/RFC1271/RFC1284/RFC1286/RFC13 05/RFC1305/RFC1305/RFC1321/RFC1323
	FCC Part 15 (CFR 47) CLASS A
	ICES-003 CLASS A
	VCCI CISPR 32 CLASS A
	CISPR 22 CLASS A
	EN 55022 CLASS A
	AS/NZS CISPR22 CLASS A
EMC	CISPR 32 CLASS A
LIVIC	EN 55032 CLASS A
	AS/NZS CISPR32 CLASS A
	CISPR 24
	EN 55024
	EN 61000-3-2
	EN 61000-3-3
	ETSI EN 300 386
Temperature	Operating temperature: 0°C to 40°C (32°F to 104°F)
	Storage temperature: -40°C to 70°C (-40°F to 158°F)
Humidity	5% to 95% (non-condensing)
Environmental protection	WEEE and RoHS
Safety	UL 60950-1



Item	Feature description
	CAN/CSA C22.2 No 60950-1
	IEC 60950-1
	EN 60950-1
	AS/NZS 60950-1
	FDA 21 CFR Subchapter J
	GB 4943.1

Performance and scalability

Item	Description	HB LPU	HF LPU
Virtualizatio	IRF2.0 stack	2	2
n	M-LAG device number	2	2
ACL	max number of ingress ACLs	40K	40K
	max number of ingress Car	8K	8K
	max number of ingress Counter	8K	8K
	max number of egress ACLs	20K	20K
	max number of egress Counter	4K	4K
Forwarding	Jumbo frame length(byte)	12288	12288
table	Mirroring group	15	15
	PBR policy	1000	1000
	PBR node	256	256
	max number of MACs per switch	750K	750K
	max number of ARP entries IPv4	350K	1M
	max ND table size for IPv6	48K	48K
	max number of unicast routes IPv4	250K	4M
	max number of unicast routes IPv6	64K	2M
	IPv4 I2 multicast group	2000	4000
	IPv4 I3 multicast group	2000	4000
	IPv4 multicast routing	16K	16K
	IPv6 I2 multicast group	1000	2000
	IPv6 I3 multicast group	1000	2000
	IPv6 multicast routing	8K	8K
	LAGG group	1024	1024
	LAGG member per group	64	64
	ECMP group	max 2047	max 2047
	ECMP member per group	2-128	2-128
	VRF	4095	4095

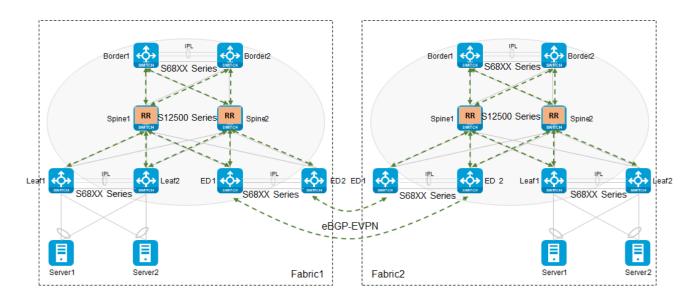


Item	Description	HB LPU	HF LPU
Interface	Loopback interface number	1K	1K
	L3 sub interface number	4094	4094
	SVI interface number	4094	4094
	VxLAN AC number	16K	16K
	VxLAN VSI number	16K	16K
	VxLAN tunnel number	4K	4K
	VSI interface number	8K	8K
	IPv4 tunnel number	127	127
	IPv6 tunnel number	127	127
	VLAN number	4094	4094
Performance	RIB	1M	4M
	MSTP instance	64	64
	PVST instance	128	128
	PVST logical port number	1000	1000
	VRRP VRID	16	16
Performance	VRRP group	256	256
	NQA group	32	32
MPLS/VPLS		128(local),256(rem	128(local),256(rem
	LDP peer	ote)	ote)
	VRF	4000	4000
	VPLS: Number of Pseudo Wires	4000	4000
	VPLS: number of peers/single VPLS full mesh		
	instance	100	100
	RSVP adjacency	200	200
Static table	static mac-address	20K	20K
	static multicast mac-address	256	256
	static ARP	8K	8K
	static ND	1K	1K
	static IPv4 routing table	250K	250K
	static IPv6 routing table	128K	128K

Data Center Application

The typical data center application is an EVPN-VxLAN design,S12500G-AF or S12500X-AF swithes work as spine or spine/border, S68XX series work as leaf and border or ED. From this design ,the users can get a non-blocking large L2 system.





Ordering information

B 1 (1B		
Product ID	Product Description	
LS-12504X-AF	H3C S12504X-AF Ethernet Switch Host	
LS-12508X-AF	H3C S12508X-AF Ethernet Switch Host	
LS-12516X-AF	H3C S12516X-AF Ethernet Switch Host	
LSXM1SUP04B1	H3C S12504X-AF Supervisor Engine Module	
LSXM1SUP04H1	H3C S12504X-AF Supervisor Engine Unit	
LSXM1SUPB1	H3C S12500X-AF Supervisor Engine Module	
LSXM1SUPH1	H3C S12500X-AF Supervisor Engine Unit	
LSXM1SFH04D1	H3C S12504X-AF Fabric Module,Type H(Class D)	
LSXM1SFH08C1	Switching Fabric Module For S12508X-AF,Type H(Class C)	
LSXM1SFH08D1	H3C S12508X-AF Fabric Module, Type H(Class D)	
LSXM1SFH08E1	Switching Fabric Module For S12508X-AF,Type H(Class E)	
LSXM2SFH16C1	H3C S12516X-AF Fabric Module, Type H(Class C)	
LSXM1SFH16C1	H3C S12516X-AF Fabric Module, Type H(Class C+)	
LSXM1SFH16E1	H3C S12516X-AF Fabric Module, Type H(Class E)	
	H3C S12500X-AF 18-PORT 100GBASE Ethernet Optical	
LSXM1CGQ18QGHF1	Interface(QSFP28)/36-Port 40GBASE Ethernet Optical Interface	
	Module(QSFP+)(HF)	
LCVM41CCO10OCLID4	H3C S12500X-AF 18-Port 100GBASE (QSFP28)/36-Port 40GBASE	
LSXM1CGQ18QGHB1	Ethernet Optical Interface Module (QSFP+)(HB)	
	H3C S12500X-AF 24-Port 10GBASE Ethernet Optical	
LSXM1TGS24QGMODHB1	Interface(SFP+,LC)+4-Port 40GBASE Ethernet Optical Interface	
	Module(QSFP+)(HB),With 1 Expansion Slot	
LSXM1CGQ36HB1	H3C S12500X-AF 36-Port 100GBASE Ethernet Optical Interface	



	Module(QSFP28)(HB)	
LCVM1OCC26LIP1	H3C S12500X-AF 36-Port 40GBASE Ethernet Optical Interface	
LSXM1QGS36HB1	Module(QSFP+)(HB)	
LSXM1TGS48HB1	H3C S12500X-AF 48-Port 10GBASE Ethernet Optical Interface	
L3XWITIG340FIBT	Module(SFP+,LC)(HB)	
LSXM1QGS48HB1	H3C S12500X-AF 48-Port 40GBASE Ethernet Optical Interface	
ESAMIQUSTOTIBI	Module(QSFP+)(HB)	
LSXM1CGQ48HB1	H3C S12500X-AF 48-Port 100GBASE Ethernet Optical Interface	
LSXIVITEGQ TOTIDT	Module(QSFP28)(HB)	
	H3C S12500X-AF 6-Port 100GBASE Ethernet Optical	
LSXM1CGQ6QGHB1	Interface(QSFP28)/12-Port 40GBASE Ethernet Optical Interface	
	Module(QSFP+)(HB)	
	H3C S12500X-AF,48-Port 10G BASE Ethernet Optical	
LSXM1TGS48C2HB1	Interface(SFP+,LC)+2-Port 100GBASE Ethernet Optical Interface	
	Module(QSFP28)(HB)	
LSXM1BFP16A	16 Fabric Blank Filler Panel	
LSXM1BFP08A	08 Fabric Blank Filler Panel	
LSXM1BFP04A	04 Fabric Blank Filler Panel	
LSXM116XFAN	H3C S12516X-AF Ethernet Switch Fan Module	
LSXM108XFAN	H3C S12508X-AF Ethernet Switch Fan Module	
LSXM104XFAN	H3C S12504X-AF Ethernet Switch Fan Module	
LSXM116XFANH	H3C S12516X-AF Ethernet Switch High Speed Fan Module	
LSXM108XFANH	H3C S12508X-AF Ethernet Switch High Speed Fan Module	
LSXM104XFANH	H3C S12504X-AF Ethernet Switch High Power Fan Module	
PSR2400-54A	AC Power Module,2400W	
PSR2400-54D	DC Power Module,2400W	
PSR3000-54A	3000W AC Power Supply Module	
PSR3000-54AHD	3000W AC & 240V-380V HVDC Power Supply	

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